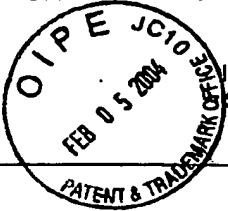


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		APPLICANT Gregory Starr	CONF. NO. 6613
		FILING DATE October 21, 2003	GROUP ART UNIT 2816

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ind	Re. 35,797	5/1998	Graham et al.	326	40	
ind	3,473,160	10/1969	Wahlstrom	326	41	
ind	4,494,021	1/1985	Bell et al.	307	591	
ind	4,633,488	12/1986	Shaw	375	120	
ind	4,719,593	1/1988	Threewitt et al.	364	900	
ind	4,857,866	8/1989	Tateishi	331	1A	
ind	4,868,522	9/1989	Popat et al.	331	2	
ind	4,959,646	9/1990	Podkowa et al.	340	825.83	
ind	5,072,195	12/1991	Graham et al.	331	2	
ind	5,075,575	12/1991	Shizukuishi et al.	307	465	
ind	5,079,519	1/1992	Ashby et al.	331	1A	

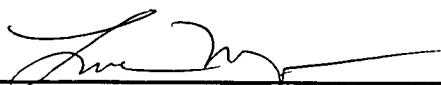
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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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ind	0 266 065	5/1988	European Pat. Off.	/	/	✓	
	0 416 930	3/1991	European Pat. Off.				
ind	0 778 517	6/1997	European Pat. Off.	/	/	✓	
ind	0 987 822	3/2000	European Pat. Off.	/	/	✓	not available

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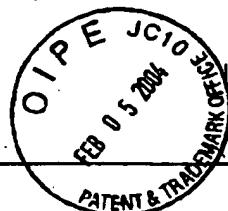
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lnd	5,121,014	6/1992	Huang	327	276	
lnd	5,133,064	7/1992	Hotta et al.	395	550	
lnd	5,204,555	4/1993	Graham et al.	307	465	
lnd	5,208,557	5/1993	Kersh	331	57	
lnd	5,239,213	8/1993	Norman et al.	326	38	
lnd	5,349,544	9/1994	Wright et al.	364	600	
lnd	5,394,116	2/1995	Kasturia	331	34	
lnd	5,397,943	3/1995	West et al.	326	39	
lnd	5,418,499	5/1995	Nakao	331	57	
lnd	5,420,544	5/1995	Ishibashi	331	11	
lnd	5,424,687	6/1995	Fukuda	331	11	

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						YES	NO
lnd	1 056 207 ,	11/2000	European Pat. Off.			✓	
lnd	1-137646 ,	5/1989	Japan			ABS	
lnd	10,215,156 ,	8/1998	Japan Pat. Abstract			ABS	

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lnd	LSI Logic Corp., 500K Technology Design Manual (Document DB04-000062-00, First Edition), pp. 8-1 - 8-33 (December 1996)
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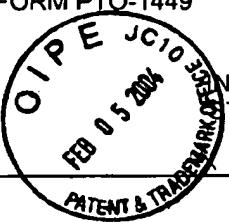
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lsw	5,448,191	9/1995	Meyer	327	105	
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lsw	5,542,083	7/1996	Hotta et. al.	709	400	
lsw	5,581,214	12/1996	Iga	331	16	
lsw	5,629,651	5/1997	Mizuno	331	34	
lsw	5,642,082	6/1997	Jefferson	331	25	
lsw	5,646,564	7/1997	Erickson et al.	327	158	
lsw	5,656,959	8/1997	Chang et al.	327	105	
lsw	5,691,669	11/1997	Tsai et al.	331	17	
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lsw	Lucent Technologies, Inc., "ORCA® Series 3 Field-Programmable Gate Arrays, Preliminary Data Sheet, Rev. 01 (August 1998)
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w	5,742,180	4/1998	DeHon et al.	326	40	
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w	5,815,016	9/1998	Erickson	327	158	
w	5,847,617	12/1998	Reddy et al.	331	57	
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lom	5,900,757	5/1999	Aggarwal et al.	327	198	
lom	5,952,891	9/1999	Boudry	331	57	
lom	5,963,069	10/1999	Jefferson et al.	327	158	
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iom	Zaks, R., et al., <u>From Chips to Systems: An Introduction to Microcomputers</u> , pp. 54-61 (Prentice-Hall, Inc., Englewood Cliffs, N.J., 1987)

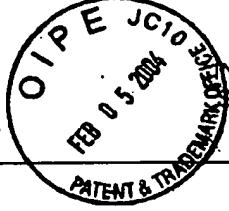
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ur	6,014,048	1/2000	Talaga, Jr., et al.	327	156	
ur	6,043,677	3/2000	Albu et al.	326	39	
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ur	6,104,222	8/2000	Embree	327	156	
ur	6,114,915	9/2000	Huang et al.	331	25	
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ur	6,144,242	11/2000	Jeong et al.	327	269	
ur	6,157,266	12/2000	Tsai et al.	331	57	

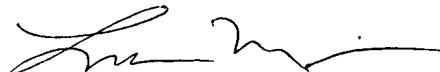
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ws	6,411,150	6/2002	Williams	327	281	
ws	6,462,623	10/2002	Horan et al.	331	17	
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